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ABSTRACT

Maste's thesis: 60 pages, 15 figures, 2 appendices, 30 sources.

SMART GRID REASONABLE ENERGY, TECHNOLOGY OF PARALLEL LOGICAL CONTROL, LOGICAL CONTROL AUTOMATICS, FUZZY LOGIC, HDL-MODEL OF AUTOMATIC.

The purpose of the qualification work is to study technologies and technical means of building intelligent energy systems Smart Grid and identify opportunities for implementation of elements of information technology parallel logic control based on control machines of parallel action with the ability to implement fuzzy inference functions.

In the course of qualification work the analysis of existing and perspective modern technologies of construction of power systems is carried out and the general characteristic of damages of electric networks and methods of their search is resulted. The architecture and principle of operation of classical control automat of parallel action are considered and the synthesis of the structure of the parallel action automat with fuzzy logic is performed. The HDL-model of the classical logical control automat of parallel action is considered, and developed an HDL model of a parallel-acting automat with fuzzy logic.

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2.2.2				
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			39
3.1 HDL-				
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3.2 HDL-				
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HDL – (., Hardware Description Language)
DSO – (., Distribution System
Operators)
Smart Grid – ,
TSO – (., Transmission System Operators)

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Smart Grid.

Smart Grid [24].

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TSO DSO, 2020
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(DSO)

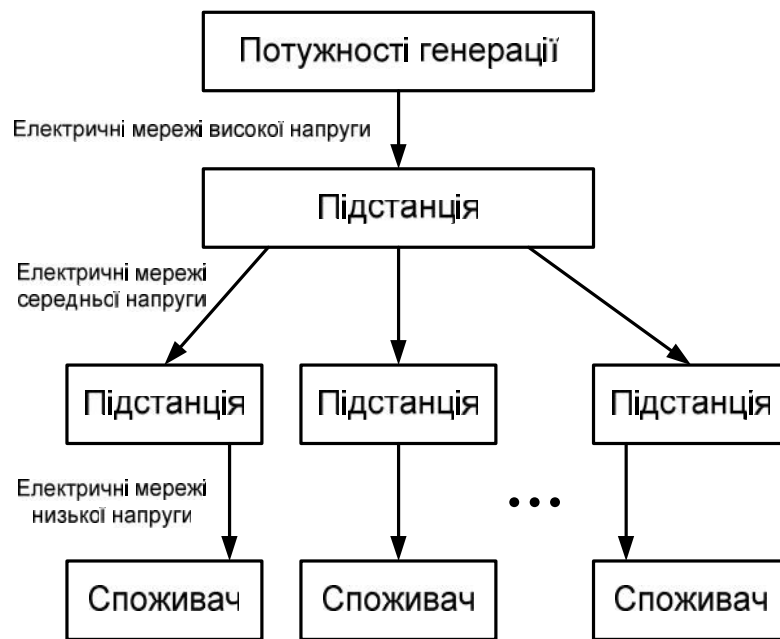
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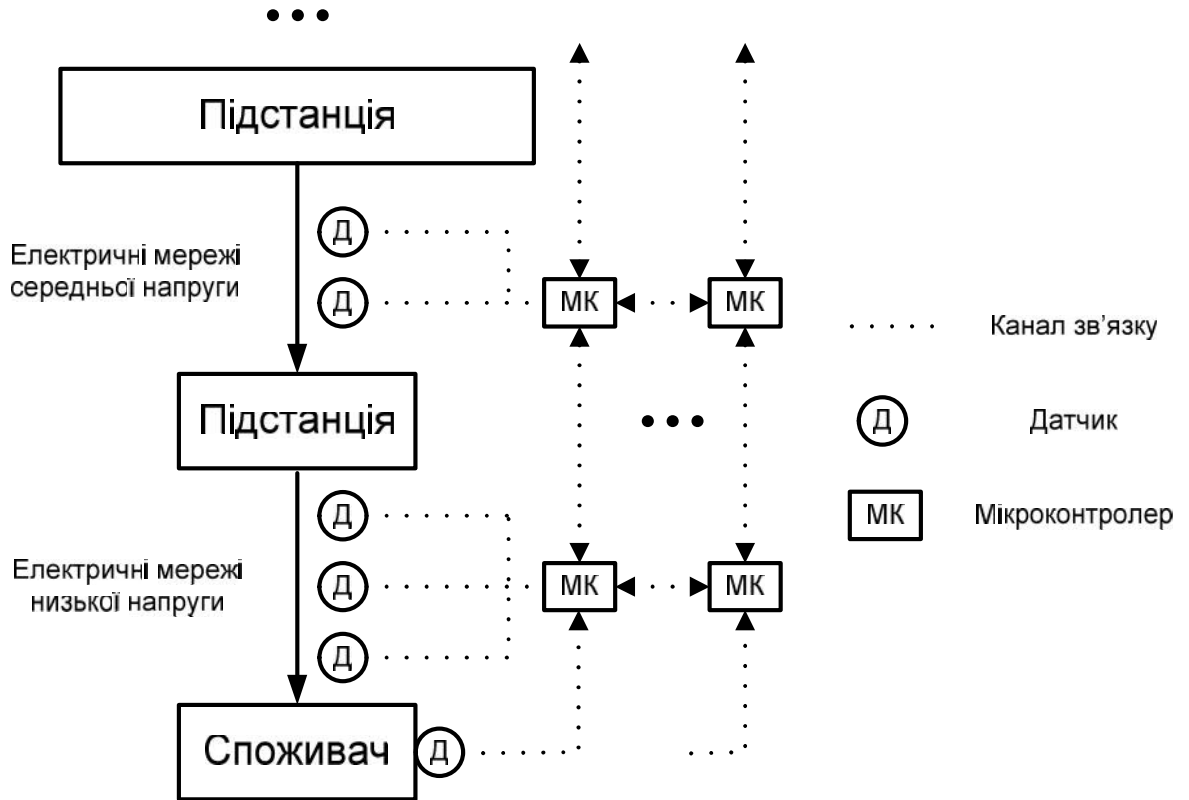
Smart Grid.

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1.2.



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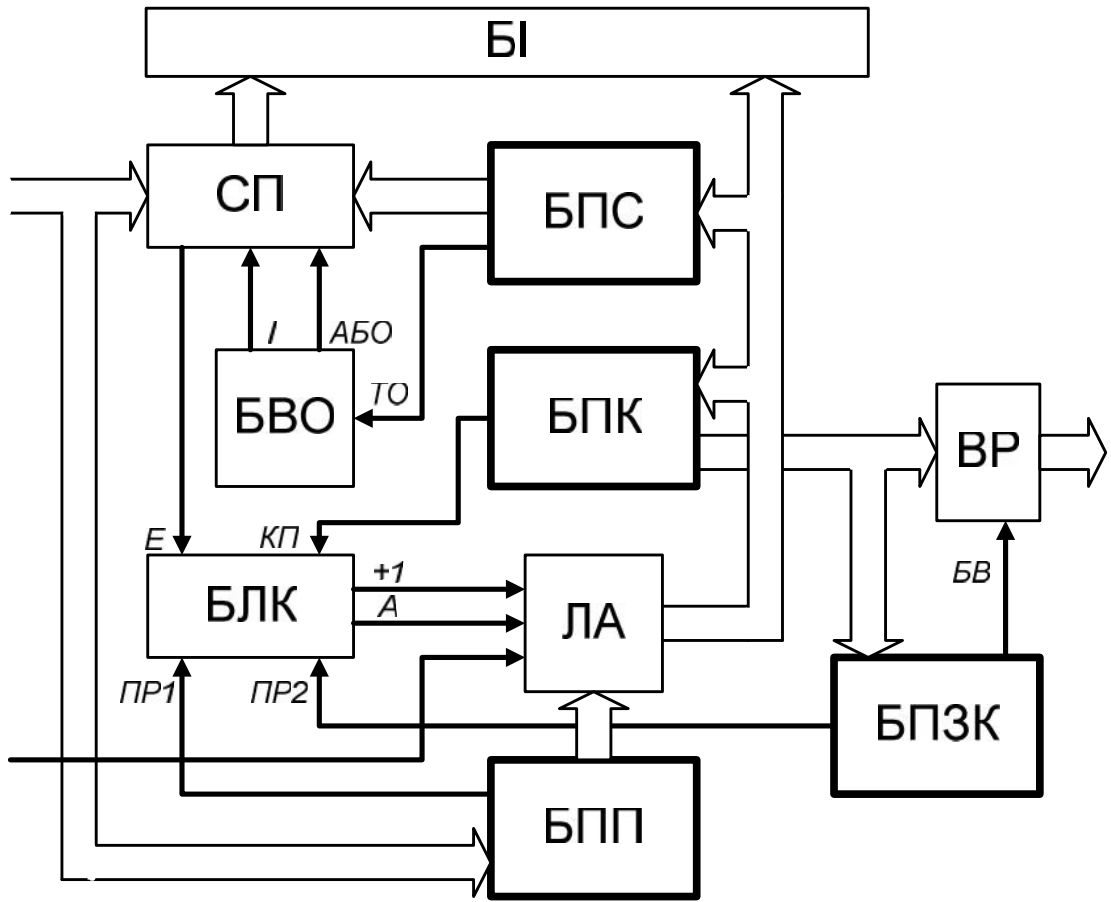
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- S4C [4].



2.1 –

S4C

2.2

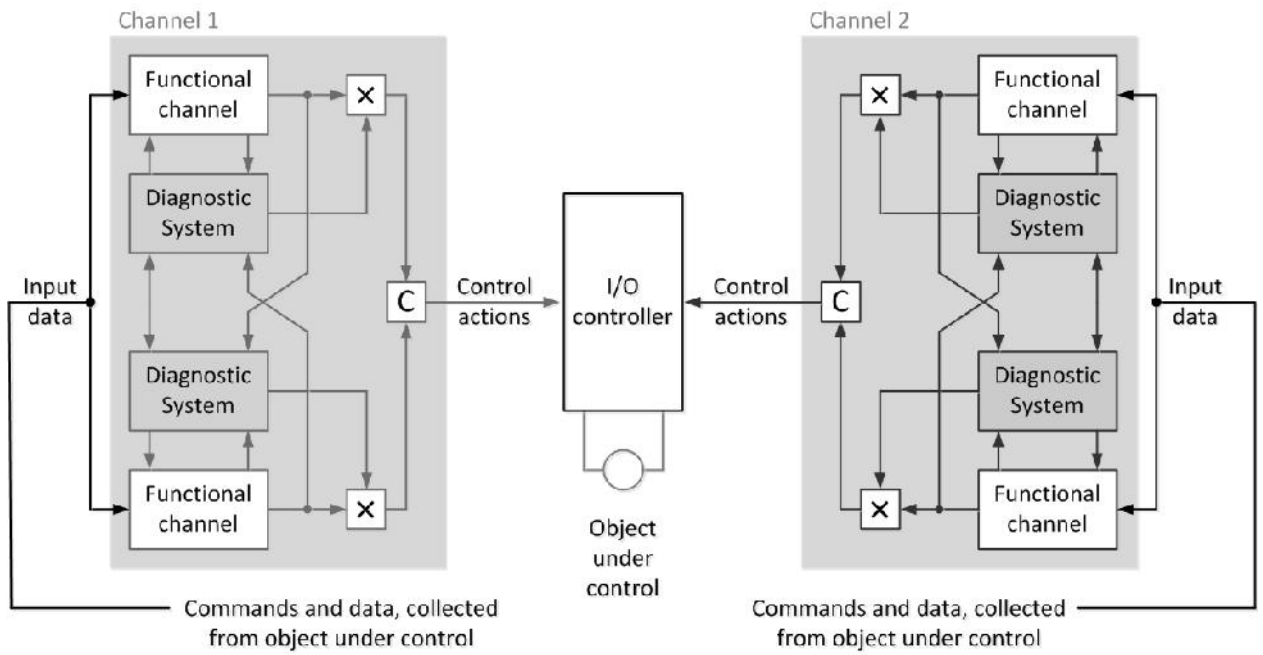
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2.3 -

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$\mu_A: X \rightarrow [0, 1]$,
 $x \in X \quad \mu_A(x) \in [0, 1]$,
 function) – (membership
) 0 1.
 $A = \{x, \mu_A(x)\}$
 $\mu_A(x)$
 $X = \{x_1, x_2, \dots, x_n\}$

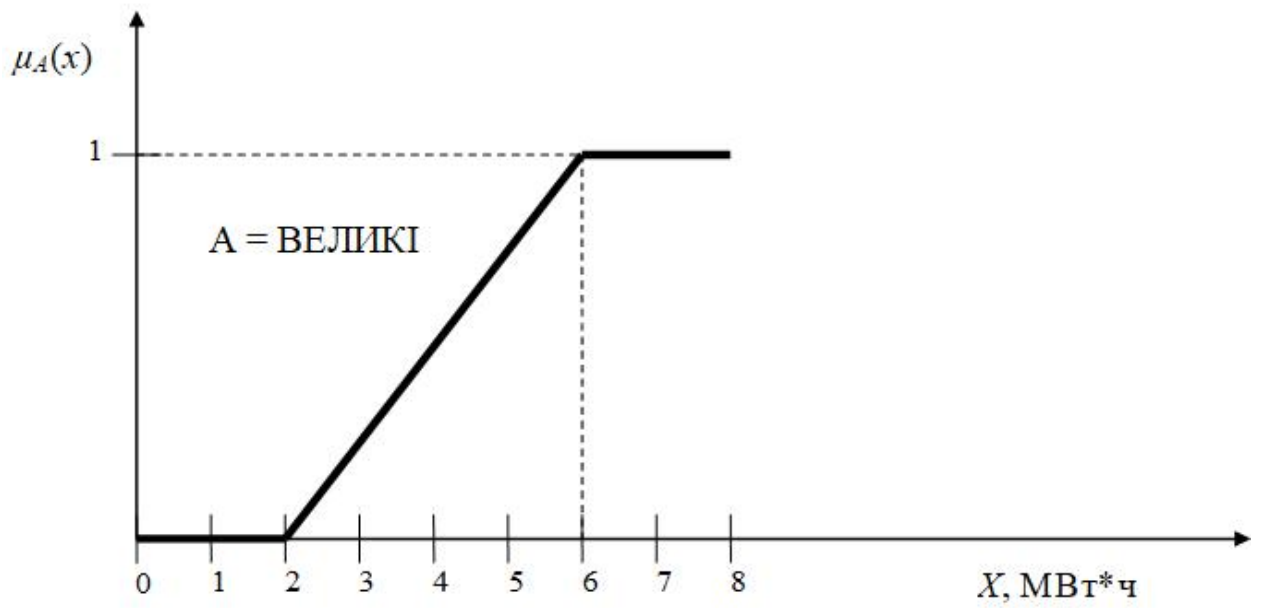
$$A = \frac{\mu_A(x_1)}{x_1} + \frac{\mu_A(x_2)}{x_2} + \dots + \frac{\mu_A(x_n)}{x_n} = \sum_{i=1}^n \frac{\mu_A(x_i)}{x_i}. \tag{2.1}$$

«/»,

$$A = \sum_{i=1}^n \sim_A(x_i) / x_i. \quad (2.2)$$

[27].

(2.4).



2.4 –

$\mu_A(x)$

2.4

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μ_A

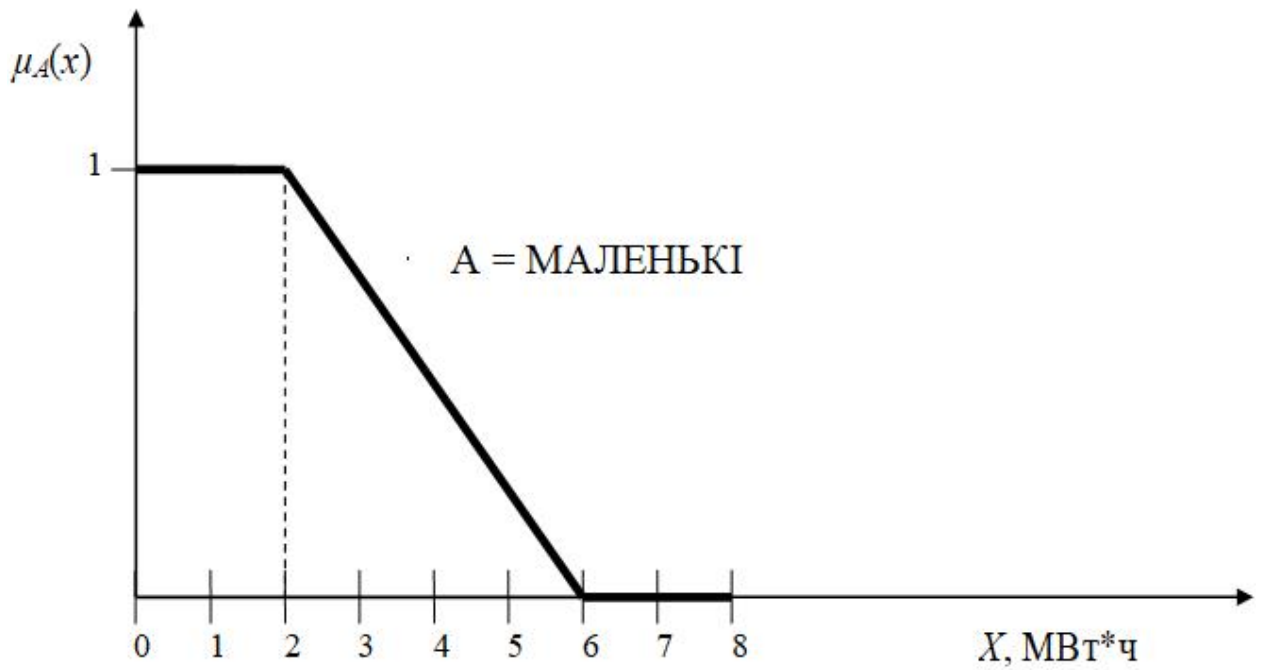
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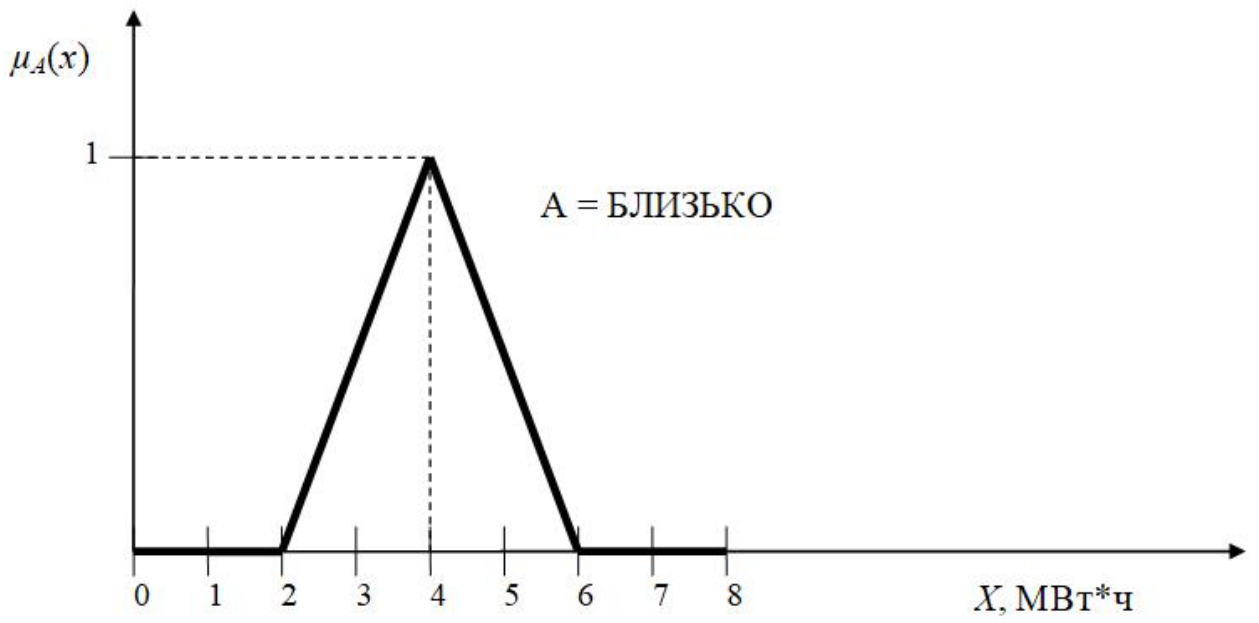
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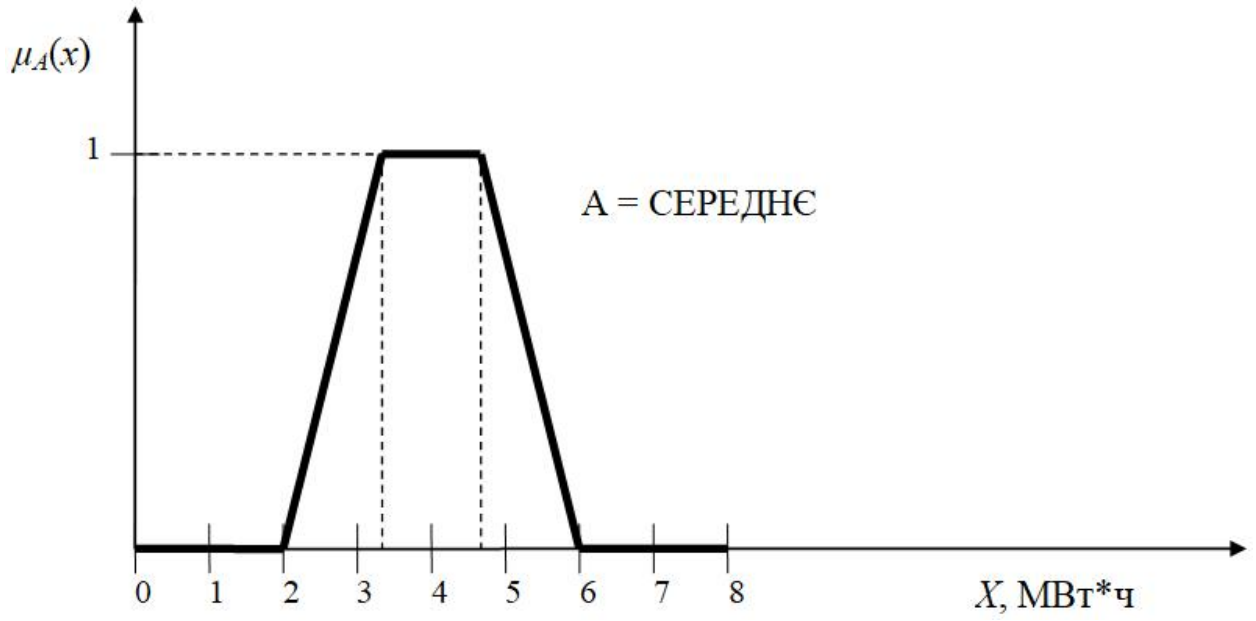
2.6 –

$\mu_A(x)$

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2.7 –

$\mu_A(x)$

2.2.2

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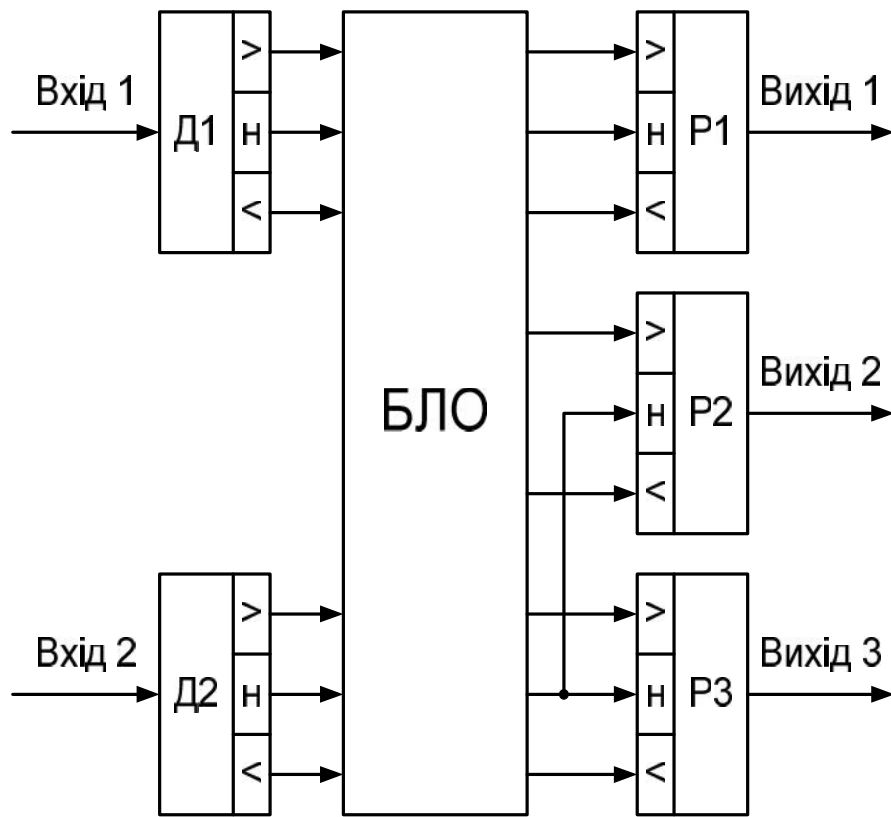
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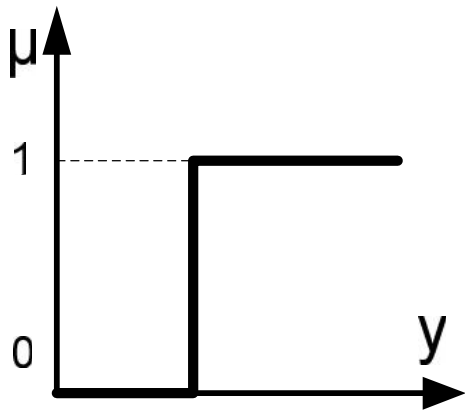
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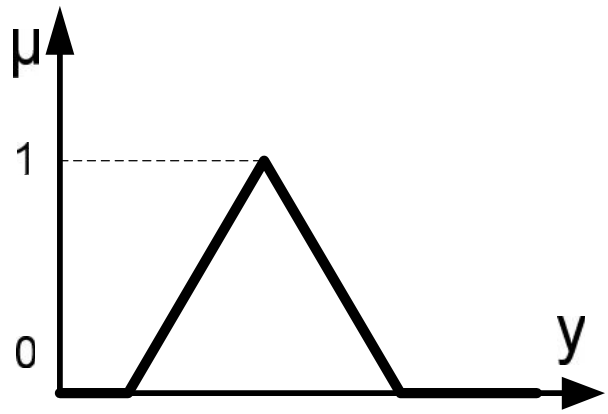
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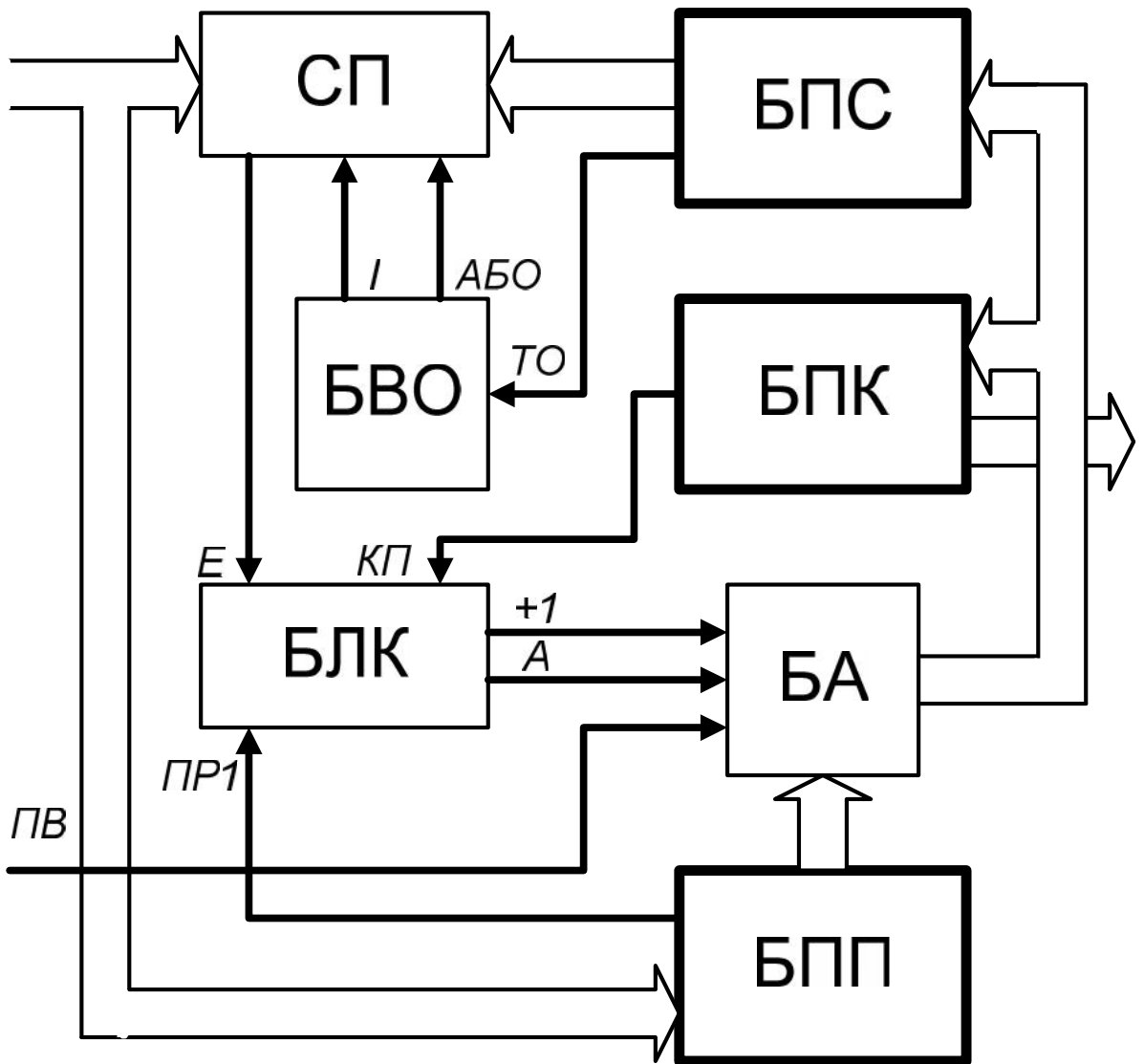
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3 HDL-

3.1 HDL-

3.1

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[3, 23].

bps: _bps –

" "

q- *adr[q..1]*, *k-*
vec_Ai[1..k][1..2] *ot* ()
 «0» «1»

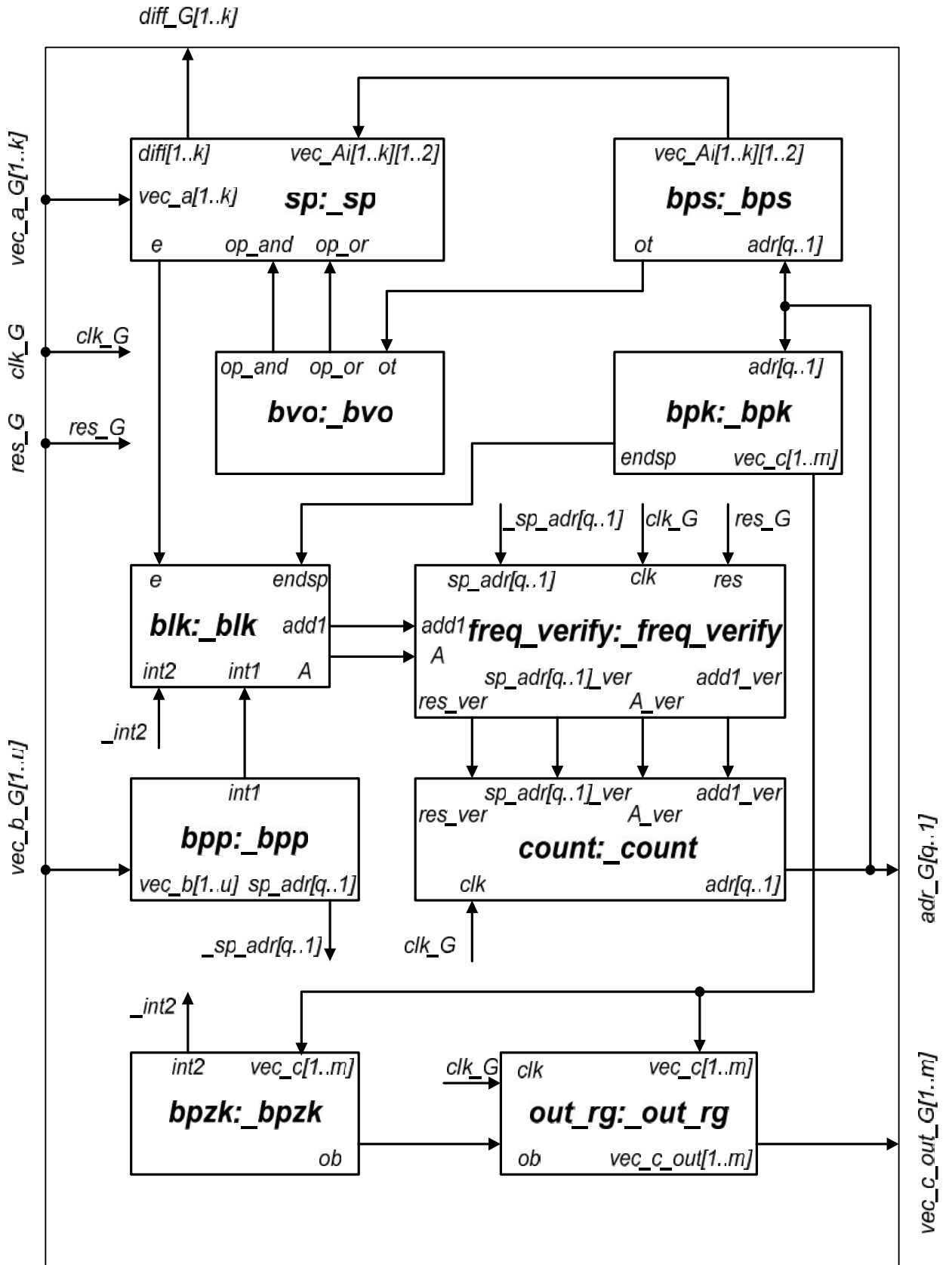
bpk: _bpk –*adr[q..1]*, *m-**vec_c[1..m]**endsp* (),

«1»,

bpp: _bpp –*u-**vec_b[1..u]*, *q-**sp_adr[q..1]* *int1* (1)

«1»

vec_b[1..u]



3.1 – HDL-

```

        bpzk:_bpzk - ,
    ,
        . m- int2
    ( 2) ob ( ),
        vec_c[1..m]
    .
        sp:_sp -
    k- vec_ [1..k] vec_Ai[1..k][1..2],
        op_and op_or ( « »,
« ») «1» e ( )
        vec_ [1..k] vec_Ai[1..k][1..2],
    .
    k- diff[1..k]
    .
        bvo:_bvo -
    , ot
    ( ) op_and ( « ») op_or (
« »).
        count:_count - ,
    , q-
    sp_adr_ver[q..1] ( ) adr[q..1], clk,
        res_ver ( ), A_ver (
    ) add1_ver ( ).
        blk:_blk - ,
    ,
    .
    e ( ), endsp ( ), int1, int2 (
    1 2), - A (
    ) add1 ( ).

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                                out_rg:_out_rg -
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                                m-
vec_c[1..m]      vec_c_out[1..m]      ,
clk              ob.
freq_verify:_freq_verify -      8-
                                ,
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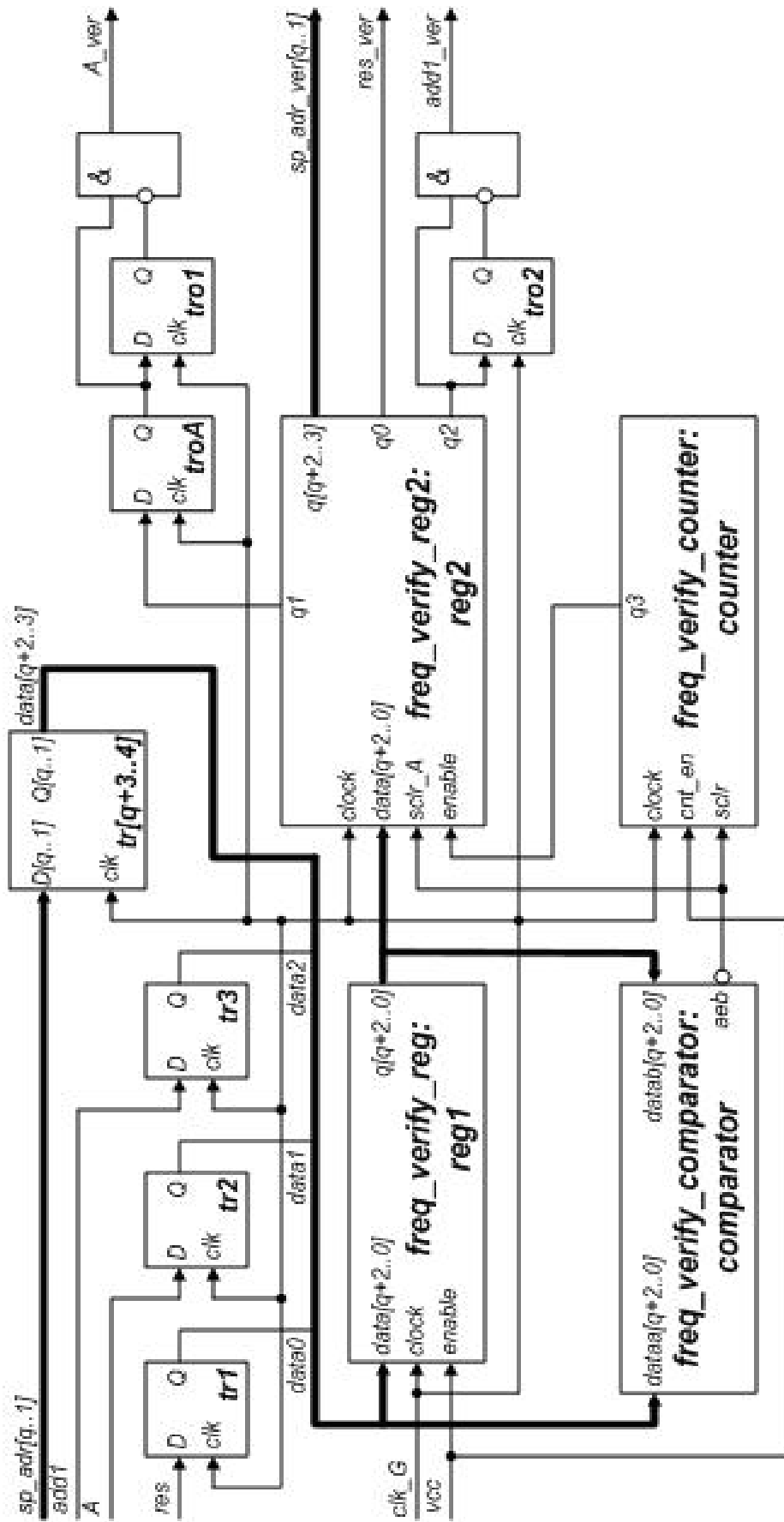
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3.2.

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                                freq_verify:_freq_verify :
-      freq_verify_reg:reg1 - (q+3)-
,      clock (
),      data[q+2..0]
q[q+2..0].
      res, A, add1 (
q      sp_adr[q..1];
-      freq_verify_reg2:reg2 - (q+3)-
,
      res, A, add1 sp_adr[q..1].
      freq_verify_reg:reg1,
sclr_A -      1- (
      A_ver).      q
sp_adr_ver[q..1],      q0
res_ver -      ;
-      freq_verify_comparator:comparator -
                                .
      dataa[q+2..0], datab[q+2..0]      aeb,
dataa[q+2..0] datab[q+2..0];

```



freq_verify: _freq_verify

```

-   freq_verify_counter:counter - 4-
-   clock (
sclr (
        «1»
        data[q+2..0]
-   tr (tr1-tr[q+3]) -
        res, A, add1 sp_adr [q..1]
        clk_G.
        D
        clk,
        Q;
-   tro (tro1, tro2) -
        « »
        ver_A ver_add1
        clk_G.
        tro
        troA -
        tr
        A_ver
        clk_G.

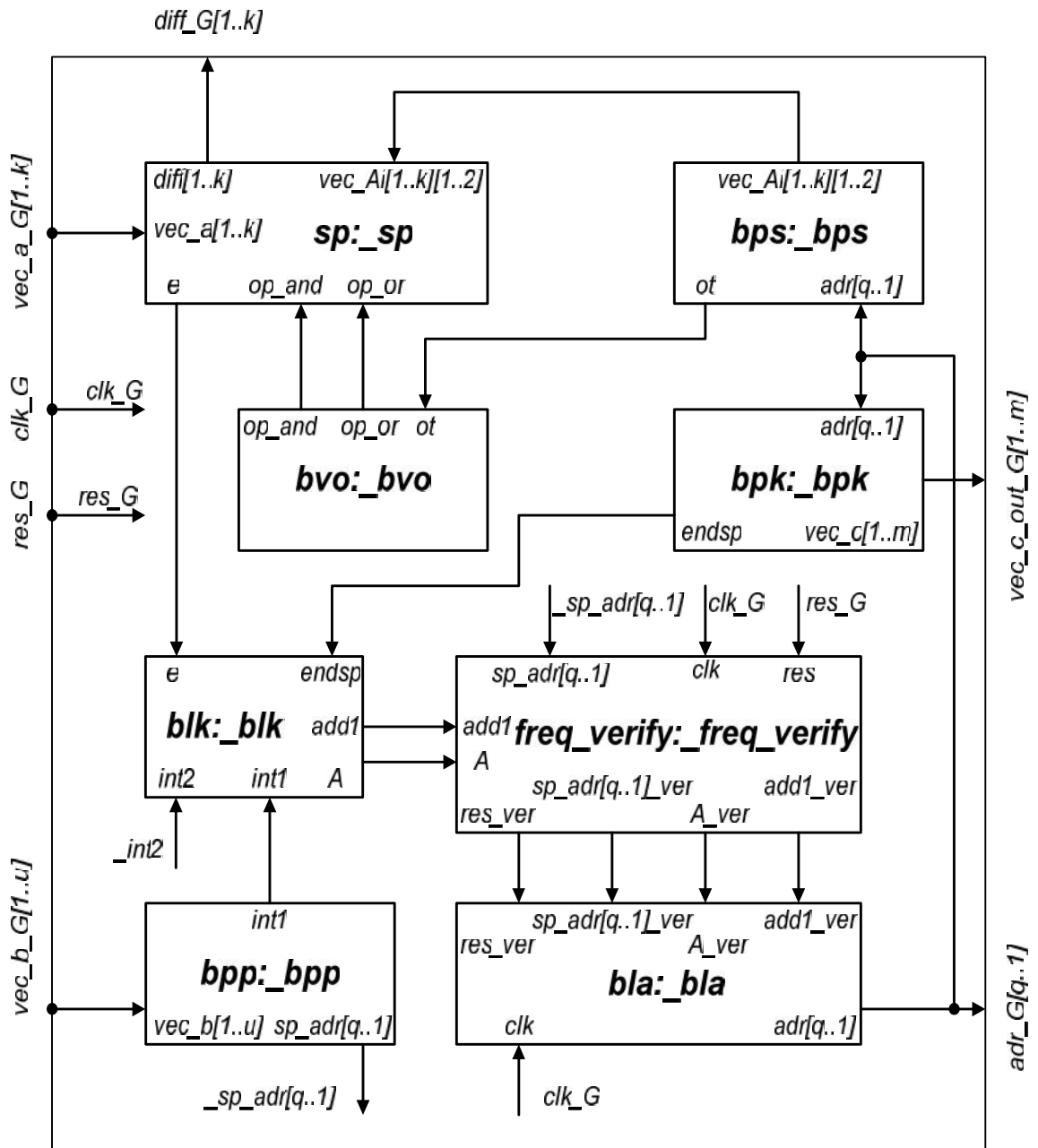
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3.2 HDL-

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        bpk:_bpk -
        q-
        adr[q..1], m-
        vec_c[1..m]
        endsp (
,
.

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3.3 – HDL-

bps:_bps –

bps:_bps

HDL-

q-

adr[q..1], *k-*

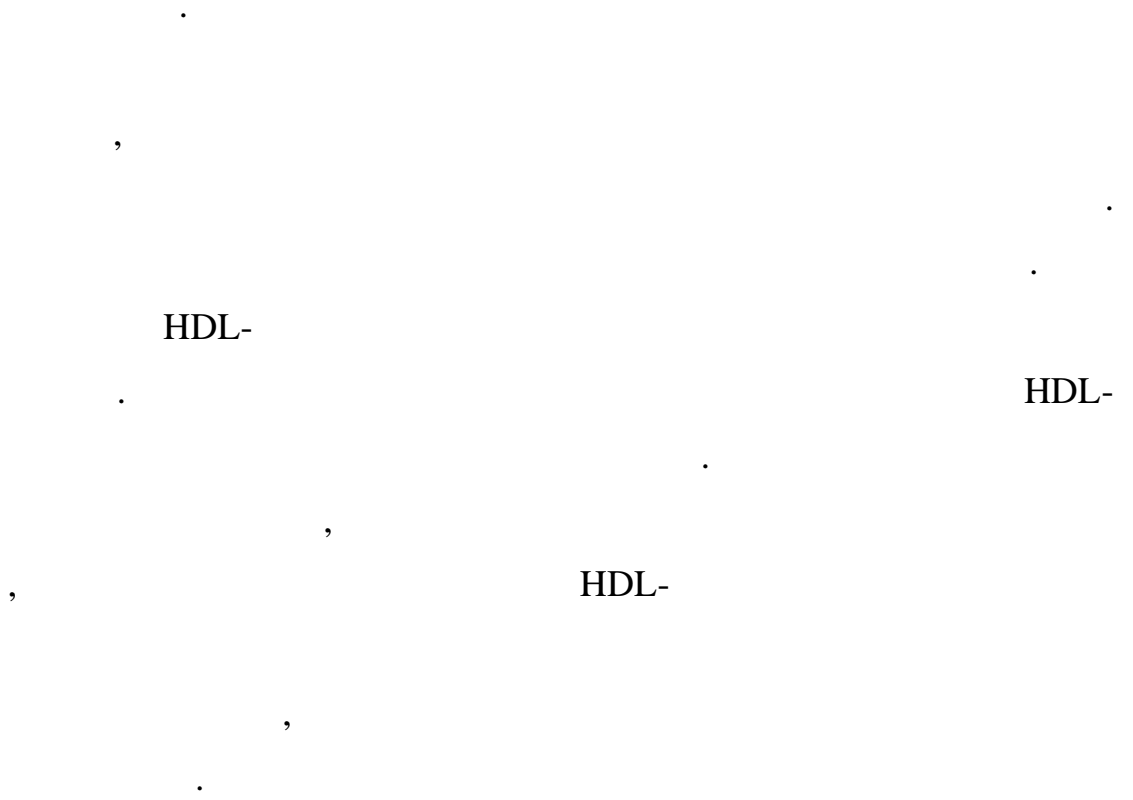
vec_Ai[1..k][1..2]

ot (

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```

        bpk: _bpk
        .
        u-
        vec_b[1..u], q-
        (
        )
        sp_adr[q..1] int1 (
        1)
        «1»
        vec_b[1..u]
        .
        sp: _sp -
        -
        k-
        vec_ [1..k] vec_Ai[1..k][1..2].
        k-
        diff[1..k]
        .
        bvo: _bvo -
        (
        « », « »).
        bla: _bla -
        ,
        .
        count: _count
        ,
        :
        bpp: _bpp,
        bps: _bps.
        blk: _blk -
        ,
        bla: _bla
        ,
        .
        e, endsp, int1, int2,
        A add1,
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        .
        freq_verify: _freq_verify -
        8-
        ,
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